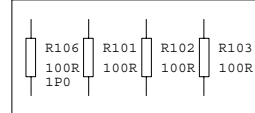
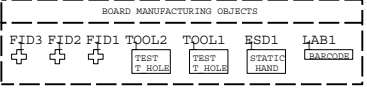
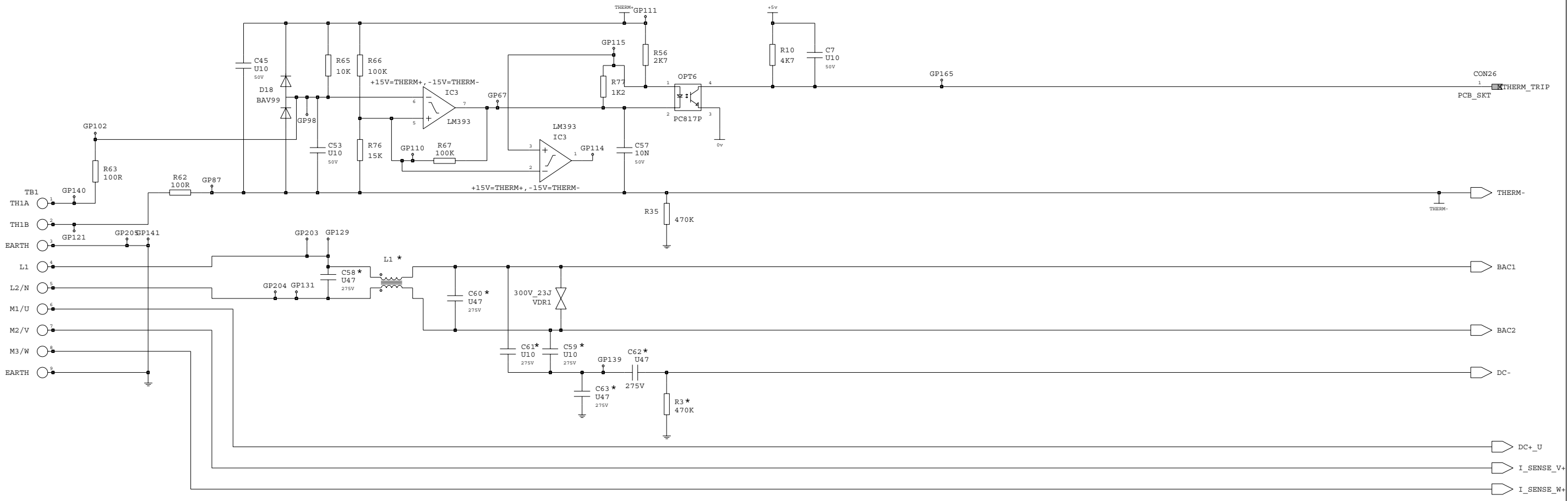
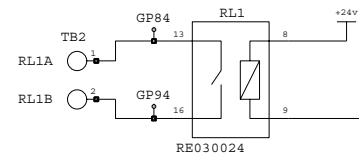


FOR COMPONENTS MARKED THUS *
PLEASE REFER TO BUILD VARIANT
TABLE ON SHEET 5 OF THIS
SCHEMATIC



EARTH BRACKET



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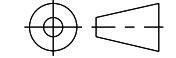
ISS	DATE	DRN	ECO	DRN	ECO	DRN	ECO	DRN	ECO	DRN	ECO	DRN	ECO
D	19JUL00	PAB		PAB		PAB		PAB		PAB		PAB	
E	26JUL00												
F	20DEC00	DCN002	GSP										
G	09FEB01			EJT									
H	21FEB01												
I	15MAR01	DCN007	PAB										
J	01MAR01												
1	17APR01												
2	13MAR02												
3	20MAR02												

MATERIAL	FINISH



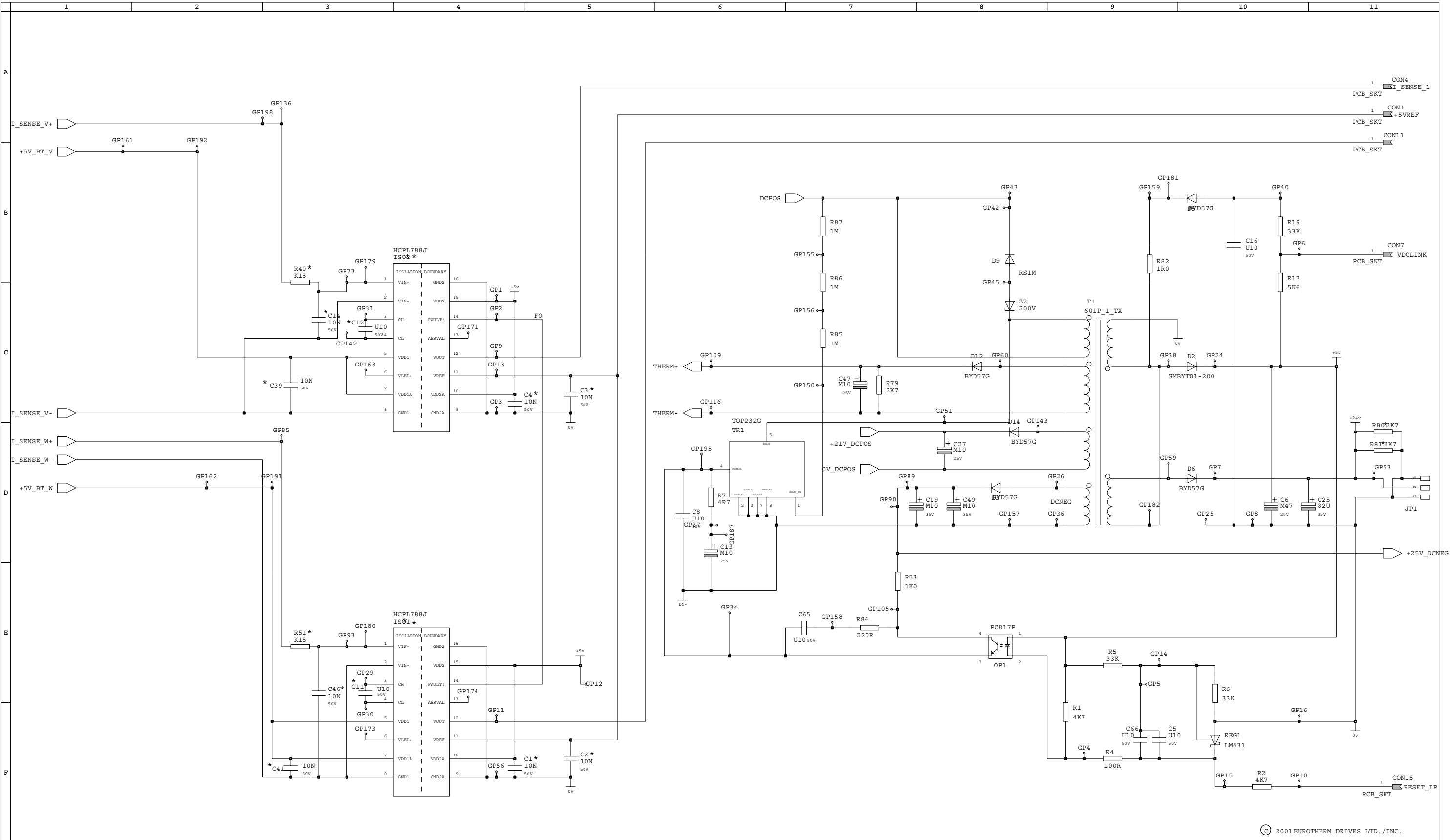
DO NOT SCALE
DIMENSIONS IN MM UNLESS
OTHERWISE SPECIFIED

BS308/ANSI Y14.5M 82
BS3939/IEEE Std91 84



DRG	SIZE	TOLERANCES	ENGINEERED	WS
A3	B	GENERAL:	CHECKED	
SCALE		HOLES:		
1:1		ANG:		
		FIRST USED		

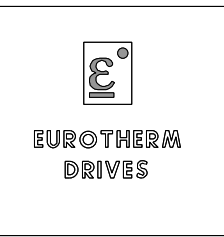
ELECTRONIC STORAGE DETAILS:		MENTOR GRAPHICS DESIGN ARCHITECT FILE	
MENTOR/INFOPLO		DESCRIPTION	
/mentor/proj/dev/650/S1em1/464718/Issue_2/AC464718_2		TITLE	
		650	
		PSU PCB SIZE 1	
DRAWING NUMBER		AI 464718	
		SHT 1	
		OF 5	



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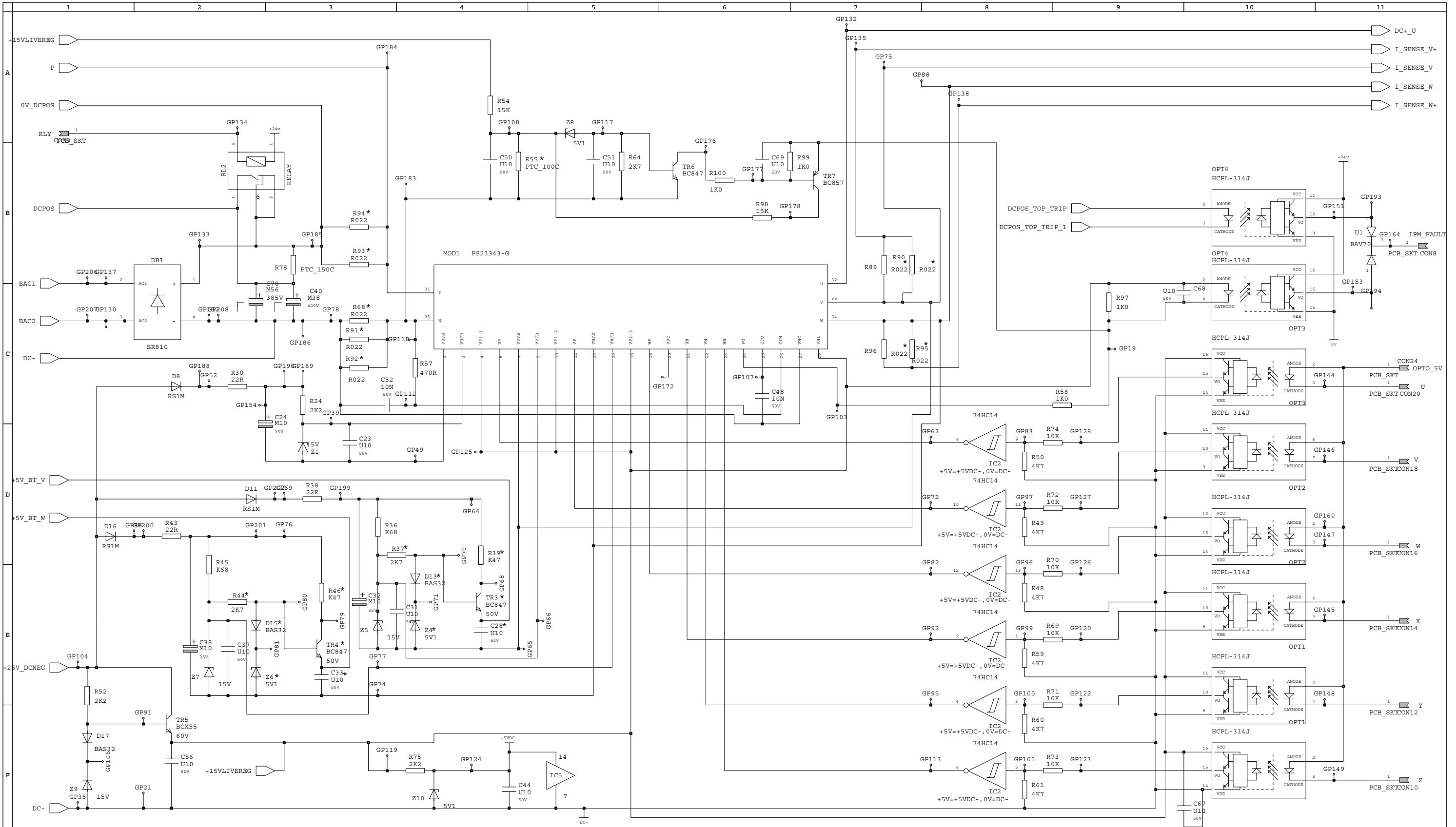
DRN	PAB	PAB	PAB	GSP	EJT	GSP	PAB	PAB	PAB	PAB	
ECO			DCN002				DCN007	15879	16683	16535	
DATE	19JUL00	26JUL00	20DEC00	09FEB01	21FEB01	15MAR01	01MAR01	17APR01	13MAR02	20MAR02	
ISS	D	E	F	G	H	I	J	1	2	3	

MATERIAL	
FINISH	



DO NOT SCALE		BS308/ANSI Y14.5M 82	
DIMENSIONS IN MM UNLESS OTHERWISE SPECIFIED		BS3939/IEEE Std91 84	
DRG	SIZE	TOLERANCES	ENGINEERED
A3	B	GENERAL:	WS
SCALE	HOLES:		CHECKED
1:1	ANG:		FIRST USED

ELECTRONIC STORAGE DETAILS:		MENTOR GRAPHICS DESIGN ARCHITECT FILE	
MENTOR/INFOPLO		/mentor/proj/dev/650/S1ze1/464718/Issue_2/AC464718_2	
DESCRIPTION		TITLE	
		650	
		PSU PCB SZE 1	
DRAWING NUMBER	AI 464718	SHT	2
		OF	5



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DRN	PAB	PAB	GSP	EJT	GSP	GSP	PAB	PAB
19JUL00	26JUL00	20DEC00	09FEB01	21FEB01	15MAR01	01MAR01	17APR01	13MAR02
D	E	F	G	H	I	J	1	2
ISS								

ECO	DRN	PAB	PAB	GSP	EJT	GSP	PAB	PAB

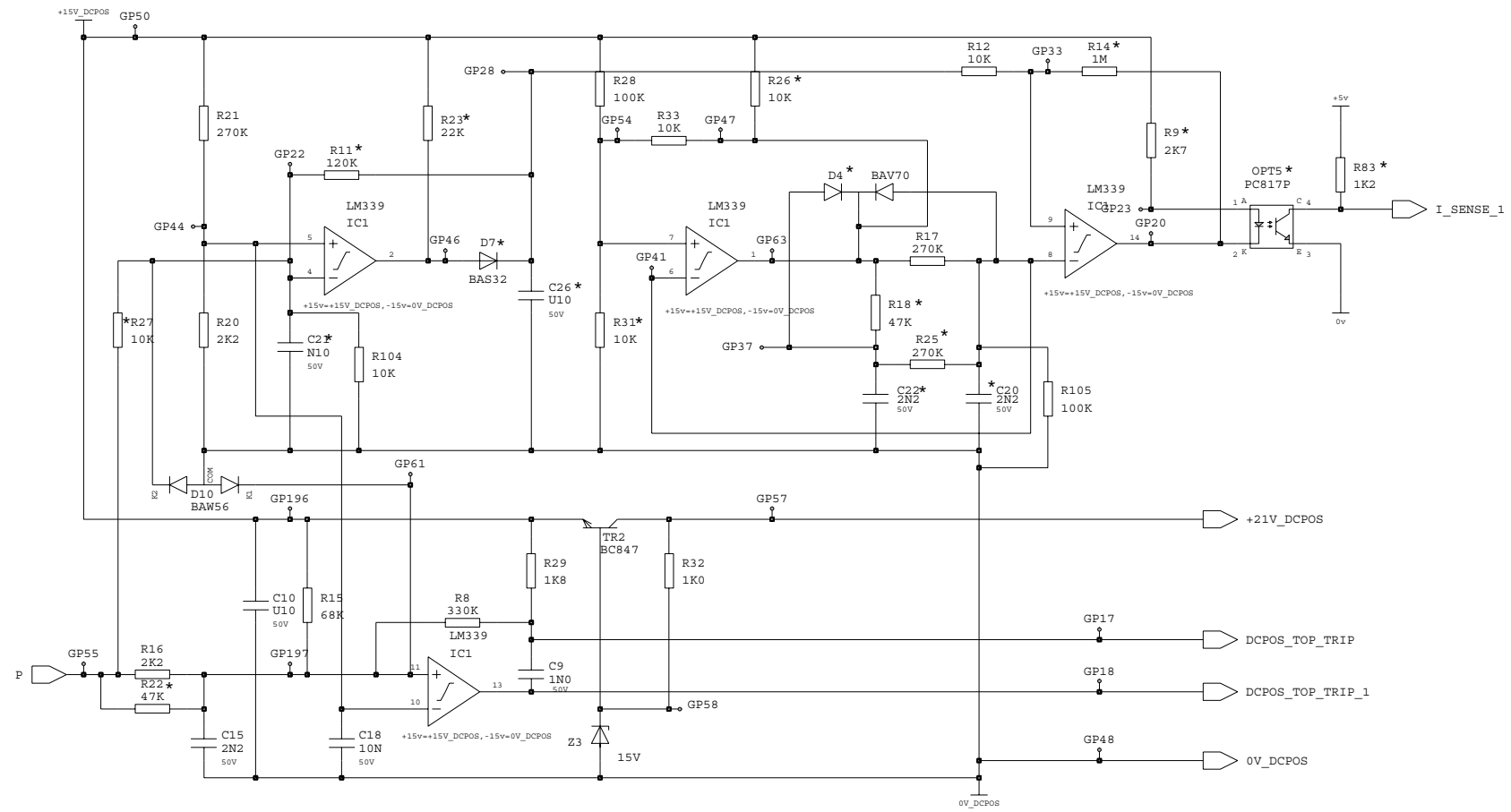
MATERIAL	
FINISH	



DO NOT SCALE
DIMENSIONS IN MM UNLESS OTHERWISE SPECIFIED

DRG 150	SIZE A3	ANGT B	TOLERANCES	ENGINEERED WS
			GENERAL:	CHECKED
			HOLES:	FIRST USED
			ANG:	

ELECTRONIC STORAGE DETAILS: MENTOR GRAPHICS DESIGN ARCHITECT FILE	
MENTOR/INFOPLOT	/mentor/proj/dev/650/Sizes/464718/Issue_2/AC464718_2
DESCRIPTION	
TITLE	650
	PSU PCB SIZE 1
DRAWING NUMBER	AI 464718
	SHT 3 OF 5



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DRN	ECC	DATE	ISS	MATERIAL	FINISH	 EUROTHERM DRIVES	DO NOT SCALE		BS308/ANSI Y14.5M 82	ELECTRONIC STORAGE DETAILS: MENTOR GRAPHICS DESIGN ARCHITECT FILE	
PAB	PAB						DIMENSIONS IN MM UNLESS OTHERWISE SPECIFIED		BS3939/IEEE Std91 84	MENTOR/INFOPL0	
	DCN002						DRG 180	SIZE A3	GENERAL:	/mentor/proj/dev/650/Size1/464718/Issue_2/AC464718_2	
	GSP						A	B	HOLES:	DESCRIPTION	
D		19JUL00	D					SCALE	TITLE		
E		26JUL00	E					1 : 1	650		
F		20DEC00	F						PSU PCB SZE 1		
G		09FEB01	G								
H		21FEB01	H								
I		15MAR01	I								
J		01MAR01	J								
1		17APR01	1								
2		13MAR02	2								
3		20MAR02	3								
									DRAWING NUMBER		SHT 4
									AI 464718		OF 5

BUILD VARIANT TABLE

Table with 17 columns (Variants RefDes, U211 U213 VF OK25 NFLT, U212 U214 VF OK25 FLT, U225 U227 SV OK25 NFLT, U226 U228 SV OK25 FLT, U311 U313 VF OK37 NFLT, U312 U314 VF OK37 FLT, U325 U327 SV OK37 NFLT, U326 U328 SV OK37 FLT, U511 U513 VF OK55 NFLT, U512 U514 VF OK55 FLT, U525 U527 SV OK55 NFLT, U526 U528 SV OK55 FLT, U711 U713 VF OK75 NFLT, U712 U714 VF OK75 FLT, U725 U727 SV OK75 NFLT, U726 U728 SV OK75 FLT) and multiple rows of variant data.

THE FOLLOWING ITEMS ARE VARIANT PART, BUT ARE NOT SPECIFIED AS SUCH IN ANY ELECTRONIC DATA. MOD1 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005 CV464741U005

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Technical drawing header block containing: DRG DRN, ECO, DATE, ISS, MATERIAL, FINISH, EURO THERM DRIVES logo, DO NOT SCALE, DIMENSIONS IN MM UNLESS OTHERWISE SPECIFIED, TOLERANCES, DRG 1:1, SIZE, GENERAL HOLES, ENGINEERED, CHECKED, FIRST USED, BS308/ANSI Y14.5M 82, BS3939/IEEE Std91 84, ELECTRONIC STORAGE DETAILS, MENTOR/INPOPLO, MENTOR GRAPHICS DESIGN ARCHITECT FILE, TITLE, 650, PSU PCB SZE 1, DRAWING NUMBER, AI 464718, SHT 5 OF 5.